

**Amendments to the Specification:**

Please amend <sup>2</sup> paragraph [0018], which begins on page 8, at line 2, as follows:

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a<sup>1</sup> As shown in FIG. 3b, in one embodiment of the present invention, the transceiving ~~circuit~~ circuitry 16 of an active smart tag device 12 preferably is active low duty cycle micropower monolithic integrated circuitry (~~MIC~~) (MMIC) that generally includes a low-power battery 40 which powers a microprocessor unit 42. Additionally, the active circuitry 16 of the smart tag device 12 may include a real time clock 44 and a memory device 46 which each are connected to the microprocessor unit 42 along a signal bus 48 to allow the tag device 12 to store data from past probe 14 inquiries. The active tag device 12 may also include simple sensors 50, such as micro electro mechanical system (MEMS) accelerometers, that are also connected to the microprocessor unit along the signal bus 48 and measure the axial acceleration of the vehicle.

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